



Digital Logic Design for GTU (III-EEE/ECE/EE/E&Tc/Comp/IT-2008 Course)

By A.P. Godse,D.A. Godse

Technical Publications 0. Softcover. Book Condition: New. First edition. 1st edition, by Godse Binary System : Digital computer and digital systems, Binary number, Number base conversion octal and hexadecimal number, Complements, Binary codes, Binary storage and register, Binary logic, Integrated circuit. Boolean Algebra and Logic Gates : Basic definition, Axiomatic definition of Boolean algebra, Basic theorem and properties of Boolean algebra, Minterms and maxterms, Logic operations, Digital logic gates, IC digital logic families. Simplification of Boolean Functions : Different types map method, Product of sum simplification, NAND or NOR implementation, Don't care condition, Tabulation method. Combinational Logic : Introduction, Design procedure, Adder, Subtractor, Code conversion, Universal gate. Combinational Logic with MSI and LSI : Introduction, Binary parallel adder, Decimal adder, Magnitude comparator, Decoder, Multiplexer, ROM, Programmable logic array. Sequential Logic : Introduction, Flip-flops, Triggering of flip-flops, Analysis of clocked sequential circuits, State reduction and assignment, Flip-flop excitation tables, Design procedure, Design of counters, Design with state equations. Registers Transfer Logic and Micro-operation : Introduction, Inter-register transfer, Arithmetic, Logic and shift micro-operations, Conditional control statements, Fixed-point binary data, Overflow, Arithmetic shifts, Decimal data, Floating-point data, Instruction codes, Design of simple computer. Registers, Counters and the Memory Unit : Introduction, Registers. Shift...

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